

INFORMATION DISCLOSURE CITATION
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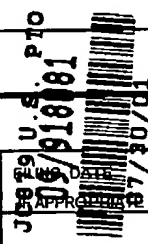
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JOSEPH MICHAEL HOKE ET AL

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	
DmB	5,287,020	02/15/94	Kimura et al			
	5,304,855	04/19/94	Naufel			
	5,487,095	01/23/96	Jordan et al			
	5,513,377	04/30/96	Capowski et al			
	5,519,737	05/21/96	Brun et al			
	5,522,088	05/28/96	Halma et al			
	5,535,377	07/09/96	Parks			
	5,568,526	10/22/96	Ferraiolo et al			
	5,598,442	01/28/97	Gregg et al			
	5,790,560	08/04/98	Durham et al			
DmB	5,832,047	11/03/98	Ferraiolo et al			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
DmB	EPO687986A2	12/20/95	EUROPE			✓	
DmB	EPO687980A2	12/20/95	EUROPE			✓	
DmB	EPO687982A1	12/20/95	EUROPE			✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DmB	J.J. Pertzborn, IBM TDB "Digital Clock Synchronization Circuit", Vol. 22, Pages 311-314

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EXAMINER *Dennis M. Butler*

DATE CONSIDERED *9-21-04*

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	
DMB	6,185,693	02/06/2001	Garmire et al			
DMB	6,192,482B1	02/20/2001	Casper et al			

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	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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